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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **KAWAMURA, Hiromitsu**

Group Art Unit: **2827**

Serial No.: **10/060,323**

Examiner: **D. E. GRAYBILL**

Filed: **February 1, 2002**

**P.T.O. Confirmation No.: 8007**

For: **SEMICONDUCTOR DEVICE AND CHIP CARRIER**

**RESPONSE TO THE RESTRICTION REQUIREMENT**  
**DATED July 28, 2004**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Date: August 26, 2004

Sir:

This paper is submitted in response to the Official Action dated **July 28, 2004**.

In the Action, restriction is required between Group (I), Claims 1-9; and Group (II),  
Claims 10-17.

Applicant hereby elects the subject matter of Group (I), Claims 1-9 for prosecution in this application. This election is made without traverse, it being understood that the applicant's rights to the filing of a divisional application directed to the non-elected subject matter under 35 USC 120 and 35 USC 121 are retained.

In the event that this paper is not timely filed, applicant hereby petitions for an appropriate extension of time. The fee for any such extension may be charged to our Deposit Account No. 01-2340.

U.S. Appln. Ser. No. 10/060,323  
Response to Restriction Requirement

In the event any additional fees are required in connection with this response, please  
charge our Deposit Account No. 01-2340.

Respectfully submitted,

ARMSTRONG, KRATZ, QUINTOS,  
HANSON & BROOKS, LLP



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